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(72) Inventor: **Gamell i Andreu, Josep**  
**08024 Barcelona (ES)**

**(74) Representative: Curell Agullà, Marcelino et al**  
**Dr. Ing. M. Curell Sunol I.I. S.L.**  
**Agentes de la Propiedad Industrial**  
**Passeig de Gracia, 65 bis**  
**E-08008 Barcelona (ES)**

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(71) Applicant: **Telesincro S.A.**  
**8290 Cerdanyola del Vallès (Barcelona) (ES)**

**(54) Integrated circuit with cryptographic capabilities**

(57) An integrated circuit comprising at least one microprocessor, an external connection interface, a data bus, an external address bus, a battery or dry cell supported data RAM (SRAM), a ROM containing at least one program and public keys, where at least part of said

program is a cryptographic algorithm operating with the public keys contained in said ROM and at least one additional circuit with predefined functionalities. One application of this printed circuit is in the production of security modules, for example, for data terminals.

FIG. 1

